EUROPEAN PATENT OFFICE

Patent Abstracts of Japan

PUBLICATION NUMBER

02141118

PUBLICATION DATE

30-05-90

APPLICATION DATE

22-11-88

APPLICATION NUMBER

63293646

APPLICANT:

CHICHIBU CEMENT CO LTD;

INVENTOR:

WATANABE ISAO;

INT.CL.

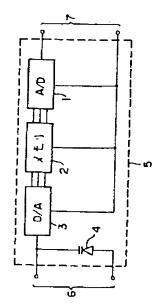
H03J 3/20 G06F 12/00 H01L 29/93 //

H01G 7/00

TITLE

VOLTAGE CONTROLLED VARIABLE

CAPACITOR



ABSTRACT:

PURPOSE: To correct the dispersion in the characteristic of a varactor diode and to improve the linearity by providing a memory circuit storing a static capacitance compensation digital code corresponding to an address designated by a digital code outputted from an A/D converter.

CONSTITUTION: An address of a memory circuit 2 that stores a static capacitance compensation digital code corresponding to an address designated by a digital code outputted from an A/D converter 1 being the result of a digital signal in the A/D converter 1 through the change in an input voltage from an input terminal 7 is selected by a digital code outputted from the A/D converter 1. Then an output digital code of the memory circuit 2 is converted into an analog signal by a D/A converter 3. Then the converted voltage output is a reverse bias fed to a varactor diode 4, resulting that the output capacitance of the varactor diode 4 is varied and the capacitance at an output terminal 6 is varied and the varactor diode is equivalent to a variable capacitor. When a function giving a linearity of the relation of voltage versus static capacitance is stored in the memory circuit, the output is corrected with a linearity. Moreover, the dispersion in the characteristic is corrected by the memory circuit.

COPYRIGHT: (C)1990, JPO& Japio